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Title: **CA2310275AA: APPARENT NETWORK INTERFACE FOR AND BETWE  
EMBEDDED AND HOST PROCESSORS**

Country: CA Canada

Kind: AA Laid-open Application <sup>1</sup>

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IPC Code: **G06F 13/00;**

ECLA Code: None

Priority Number: Nov. 26, 1997 **US1997000066583**  
Feb. 25, 1998 **US1998000030411**  
Nov. 12, 1998 **WO1998000024362**

Abstract:





An apparent network interface (30) permits an embedded processor (20) to communicate to a host processor (2) using standard network communication mechanisms/protocols such as TCP/IP, NFS, FTP, HTTP, etc. The web server protocol HTTP is particularly useful because it permits the embedded computer to publish a user interface for remote monitoring and remote control using a standard web browser application. The invention provides the host computer(10) with an apparent network interface (30) that appears to be a standard network device, such as an Ethernet interface card. This apparent network interface (30) communicates directly with the embedded processor (20) which appears to be a device on the apparent network. Significant cost savings and performance enhancements are realized by implementing the communication directly over the host computer's peripheral bus (6) rather than using standard network hardware such a Ethernet hardware.

INPADOC  
Legal Status:

| Gazette date | Code | Description (remarks)          | List all possible codes for CA |
|--------------|------|--------------------------------|--------------------------------|
| May 16, 2000 | REFW | Corresponds to pct application |                                |

Designated Country: AL AM AP AZ BA BB BG BR BY CA CN CU CZ EA EE GE GH GM HU ID IL IS KE  
KG AT BE CH CY DE DK ES FI FR GB GR IE IT

Family:

| Buy PDF   | Patent                          | Pub. Date     | Filed         | Title   |
|---|---------------------------------|---------------|---------------|---|
|  | <a href="#">WO9927456A1</a>     | June 3, 1999  | Nov. 12, 1998 | APPARENT NETWORK INTERFACE FOR AND BETWEEN EMBEDDED AND HOST PROCESSORS |
|  | <a href="#">US20020019924A1</a> | Feb. 14, 2002 | Oct. 15, 2001 | Flexible processing hardware architecture                               |
|  | <a href="#">US6308234</a>       | Oct. 23, 2001 | Feb. 25, 1998 | Flexible processing hardware architecture                               |
|  | <a href="#">US6058434</a>       | May 2, 2000   | Feb. 19, 1998 | Apparent network interface for and between embedded and host processors |
|   | <a href="#">JP2001524713T2</a>  | Dec. 4, 2001  | Nov. 12, 1998 |   |
|   | <a href="#">EP1032884A1</a>     | Sept. 6, 2000 | Nov. 12, 1998 | APPARENT NETWORK INTERFACE FOR AND BETWEEN EMBEDDED AND HOST PROCESSORS |
|   | <a href="#">CA2310275AA</a>     | June 3, 1999  | Nov. 12, 1998 | APPARENT NETWORK INTERFACE FOR AND BETWEEN EMBEDDED AND HOST PROCESSORS |
|   | <a href="#">AU1460199A1</a>     | June 15, 1999 | Nov. 12, 1998 | APPARENT NETWORK INTERFACE FOR AND BETWEEN EMBEDDED AND HOST PROCESSORS |
|   | <a href="#">AU0743997B2</a>     | Feb. 14, 2002 | Nov. 12, 1998 | APPARENT NETWORK INTERFACE FOR AND BETWEEN EMBEDDED AND HOST PROCESSORS |
| 9 family members shown above  |                                 |               |               |   |

Other Abstract Info: DERABS G1999-404771



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